

**UNIVERSITY COLLEGE OF ENGINEERING (AUTONOMOUS)**

**OSMANIA UNIVERSITY, HYDERABAD**

**EXAMINATION TIME TABLE**

**B. E. III/IV (All Branches) II - Semester (Main)**

May/June 2015

[FOR OLD SCHEME CANDIDATES]

**Time: 10.00 AM TO 1.00 PM.**

**OLD BATCH  
LAST CHANCE**

| DAY & DATE             | CIVIL | E.E.E. | E.C.E.                               | B.M.E | MECHANICAL | C.S.E |
|------------------------|-------|--------|--------------------------------------|-------|------------|-------|
| 16-05-2015<br>SATURDAY | ===== | =====  | Integrated Circuits and Applications | ===== | =====      | ===== |
| 19-05-2015<br>TUESDAY  |       | =====  | Microcontrollers and Applications    | ===== | =====      | ===== |

**B. E. III/IV (All Branches) I - Semester (Suppl.)**

[FOR OLD SCHEME CANDIDATES]

**Time: 02.00 PM TO 5.00 PM.**

**OLD BATCH  
LAST CHANCE**

| DAY & DATE             | CIVIL | E.E.E. | E.C.E.                           | B.M.E | MECHANICAL | C.S.E |
|------------------------|-------|--------|----------------------------------|-------|------------|-------|
| 06-06-2015<br>SATURDAY | ===== | =====  | Analog Electronics-II            | ===== | =====      | ===== |
| 11-06-2015<br>THURSDAY | ===== | =====  | Microprocessor and System Design | ===== | =====      | ===== |

**B. E. II/IV (All Branches) I - Semester (Suppl.)**

[FOR OLD SCHEME CANDIDATES]

**Time: 02.00 PM TO 5.00 PM.**

**OLD BATCH  
LAST CHANCE**

| DAY & DATE           | CIVIL | E.E.E.                                  | E.C.E. | B.M.E | MECHANICAL | C.S.E |
|----------------------|-------|---|--------|-------|------------|-------|
| 12-06-2015<br>FRIDAY | ===== | Electrical Measurements and Instruments | =====  | ===== | =====      | ===== |

  
**DIRECTOR OF EVALUATION**  
**EXAMINATION CELL**